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MC68HC11: An Introduction - Software and Hardware Interfacing, 2nd Edition [Han-Uei Huang, Leo Chartrand] on Amazon.com. *FREE* shipping on qualifying offers. The new, second edition of this comprehensive book on the 68HC11 microcontroller from Motorola is unique in that it uses both the assembly language (for better hardware control and execution time) and the C language (for faster ...arithmetic core lphaAdditional info:FPGA provenWishBone Compliant: NoLicense: LGPLDescriptionRTL Verilog code to perform Two Dimensional Fast Hartley Transform (2D-FHT) for 8x8 points.Presented algorithm is FHT with decimation in frequency domain.Main FeaturesHigh Clock SpeedLow Latency(97 clock cycles)Low Slice CountSingle Clock Cycle per sample operationFully synchronous core with positive ...